ISSN: 2454-9940



INTERNATIONAL JOURNAL OF APPLIED SCIENCE ENGINEERING AND MANAGEMENT

E-Mail : editor.ijasem@gmail.com editor@ijasem.org



IMPLEMENTATION OF POLAR ENCODING FOR 3GPP WITH INTEGRATED CRC GENERATION AND RATE MATCHING

MRS. S. JYOTHI¹, A.UMA MAHESWARARAO², V. MEGHANA³, A. LAKSHIMI SRAVANTHI⁴, M. SRI RAM⁵, G. SRIKAR⁶

> ¹Assistant Professor, Dept. Of ECE, PRAGATI ENGINEERING COLLEGE ²³⁴⁵⁶UG Students, Dept. Of ECE, PRAGATI ENGINEERING COLLEGE

ABSTRACT

Polar codes have been adopted as the channel coding scheme for the control channel in the 5G New Radio (NR) standard defined by 3GPP due to their capacity-achieving properties.

This paper presents an efficient implementation of Polar Encoding for 3GPP, integrating Cyclic Redundancy Check (CRC) generation and Rate Matching to enhance performance and simplify hardware design. The proposed architecture optimizes encoding efficiency by seamlessly incorporating CRC bits before the polarization process, ensuring improved error detection. Additionally, the rate matching mechanism effectively we can adjusts the code length to meet varying transmission constraints. Simulation results demonstrate the effectiveness of the integrated approach, showcasing reduced hardware complexity while maintaining reliable communication performance.

INTRODUCTION

Polar codes have gained significant attention as one of the most efficient error-correcting codes, especially after their adoption in the 3rd Generation Partnership Project (3GPP) 5G New Radio (NR) standard. Their capacity-achieving performance and low-complexity decoding make them ideal for modern wireless communication systems. However, implementing polar encoding efficiently requires careful consideration of various factors such as CRC (Cyclic Redundancy Check) generation and rate matching, both of which play a crucial role in ensuring reliable data transmission and error correction.

In this work, we focus on the implementation of polar encoding for 3GPP, integrating CRC generation and rate matching to enhance the efficiency of the encoding process. CRC-aided polar codes improve the performance of successive cancellation list (SCL) decoding by selecting the most likely correct codeword. Additionally, rate matching ensures adaptability to different channel conditions by puncturing, repetition, or shortening bits as required by the 3GPP standard.

By integrating CRC generation and rate matching into the polar encoding framework, we aim to optimize the balance between coding efficiency, error detection capability, and adaptability to varying transmission rates. This implementation is crucial for 5G NR applications, enabling robust and efficient communication in next-generation wireless networks.

LITERATURE SURVEY

Polar Code Construction and Encoding Techniques:

E. Arikan (2009) introduced the concept of polar codes, demonstrating their capacity-achieving properties using channel polarization. Since then, several works have enhanced the construction methods. Tal and Vardy (2011) proposed improved polarization techniques using successive cancellation list (SCL) decoding, significantly

INTERNATIONAL JOURNAL OF APPLIED SCIENCE ENGINEERING AND MANAGEMENT

reducing error rates. Meanwhile, Zhang et al. (2017) optimized polar code construction for short block lengths, crucial for low-latency applications in 5G.

Successive Cancellation and List Decoding:

Successive cancellation (SC) decoding is the fundamental decoding technique for polar codes, but its errorcorrecting performance can be suboptimal. Tal and Vardy (2015) introduced successive cancellation list (SCL) decoding, which improves performance by considering multiple decoding paths. Niu et al. (2018) further refined SCL decoding by integrating cyclic redundancy check (CRC), leading to CRC-aided SCL (CA-SCL) decoding, which enhances error correction, especially for short to moderate code lengths.

CRC Integration for Performance Enhancement:

The integration of CRC with polar codes enhances error detection and improves decoding reliability. Trifonov (2016) analyzed the role of CRC-aided polar codes in reducing mis corrections in SCL decoding. Li et al. (2019) explored CRC-bit selection strategies, optimizing their placement within polar codeword structures to maximize performance under various noise conditions in 3GPP applications.

Rate Matching Strategies for 3GPP Compliance:

Rate matching ensures that polar codes meet varying block length and rate requirements in practical communication systems. Bigoli et al. (2018) proposed interleaving and puncturing methods to adapt polar codes for flexible rate configurations in 5G. Sasaki et al. (2020) investigated bit-reversal-based rate matching, which improves decoding efficiency by maintaining the inherent polarization properties of the code.

PROPOSED SYSTEM

The proposed system focuses on the efficient implementation of polar encoding for 3GPP by integrating Cyclic Redundancy Check (CRC) generation and rate matching within the encoding process. This approach aims to enhance error detection, improve decoding performance, and ensure adaptability to varying transmission rates, making it suitable for 5G New Radio (NR) applications.

Key Features of the Proposed System:

1. Polar Encoding for 3GPP Standard:

- Implements systematic polar encoding as per the 5G NR standard, ensuring compliance with modern wireless communication requirements.
- o Utilizes bit-channel polarization to enhance error correction capability.

2. Integrated CRC Generation:

- A CRC-aided polar code is incorporated to enhance error detection and aid in improving the reliability of Successive Cancellation List (SCL) decoding.
- CRC bits are appended before encoding to help identify the most probable correct codeword during decoding.

3. Rate Matching for Transmission Adaptability:

• Includes a rate matching module to adapt the encoded data to different channel conditions and code rates.

www.ijasem.org

Vol 19, Issue 1, 2025

• Supports puncturing, repetition, and bit shortening techniques to ensure optimal utilization of bandwidth and improved system flexibility.

4. Efficient Hardware Implementation:

- o Optimized hardware architecture for low-latency and power-efficient encoding.
- Parallel processing techniques to accelerate encoding, CRC computation, and rate matching.

SIMULATION RESULTS

💶 Wave																						
File Edit View Add Form	nat Tools Bookmarks Window	w Help																				
Wave - Default									_	2000												: + et x
🖻 • 🗃 🖬 🛸 🎒 🐰	BB2210-A₽			tet	1.1.2	£ 🟦	9.9.	9 B.	4	۵ 🖽 (G 💠	:12 B Þ	占	₽ F ₹	125.	F				
] 34 • •€ • ∰• Search: [● 總魏 ▼			1.13	5	B 🕫			100 r	13 🛊 🖹	E	1	1	10	0,0	Q Q Q B						
€.	Msgs																					
🍁 dk 👍 rst	St1 St0		h														- n					- III
Infor_i infor_valid	01010011010100100100 St0	0)0101001	101010	0100100																		
<pre>infor_valid_reg </pre>	0 01010011010100100100100	-00	1001101		100																	
- CRC																						
data2_crc load data2 crc	00000000000000000000000000000000000000	-00101	10)01 (10		.)01 l	11 J10 J	01 I10.	101	10	00)01 J10.		b1)10.	1000		ф					
data_ont		0 [19	18)17 (16	115)14	13 J12 J	11 (10)jg	8	7)6)5	(4	3)2	11	6						
C-2 state	0 stn	0_1	-		-	-									_	2	0					
🖕 d_finish	St0	_																				
ger_2xx 🛟 🖪	101010111100010011110010	00000000	odicio	l10 l11	[10	.)11 1)1)()1)(01)11		11	01 110.		loo)oo.	111	01 11010	101 (0000	000000000	00000000	0000		
crc_out_msb_lsb New Divider	010100110101001001001		00)aa (aa	100	300000	10 1000001	10 1 00.)00		00	Xaadaaaaa.	100	2000000)00.	/0000000	010100110	10100100	10010101	0111100010	01111001	.0
encoded_o	110110111000110101111		00)11 Joc	I [11	.)00000	10 <i>)</i> 111111	11100.)11	. loo l	11)aoàoaao.		2000000.	/11		110110111	00011010	11111010	0101011101	10100001	.0000110
E-√ ac_out	010100110101001001001		00	loo loo		Jaccoci	10 (00000)	00 1 00.)00		00	0000000.		2000000			010100110	10100100	10010101	0111100010	01111001	.0
polar_trans		000000000000000000000000000000000000000		111 100	I lii	Jaccoci	10 <u>)</u> 111111	11 100.)11		11)aapaaaa.		20000000			110110111	00011010	11111010	0101011101	10100001	0000110
■-☆ x	000000000001001100010 1	000000x00	. 00)ao (ac	100)000000	io 1000001	10 JOO.)00	3003	00	Xaadaaaaa.		20000000		10000000	0000000	00100110	00101010	1110111000	00000010	1010101
encode_done	St1																					
	110110101100110101101		00)11 (OC	[11	100000	10 <u>J</u> 11111	11700.)11		11	laabaaaa.	(11	20000000	/11.		110110101	10011010	11011111	1010100000	01010100	0110110

Figure.1 Simulation Info Message



Figure.2 Schematic Polar Encode Integrated CRC Generation and Rate matching



Figure.3 Schematic CRC 2 Encode



www.ijasem.org

Vol 19, Issue 1, 2025



Figure.4 Schematic Encode 2 Rate Match

+	+	+	+	++
Site Type	Used	Fixed	Available	Util%
Slice LUTs*	113	. 0	303600	0.04
LUT as Logic	113	0	303600	0.04
LUT as Memory	0	0	130800	0.00
Slice Registers	117	0	607200	0.02
Register as Flip Flop	117	0	607200	0.02
Register as Latch	0	0	607200	0.00
F7 Muxes	0	0	151800	0.00
F8 Muxes	0	0	75900	0.00
+	+	+		+ - +

Figure.5 Utilization report

Max Delay Paths	
Slack: Source:	<pre>inf crc_out_msb_lsb_reg[11]/C (rising edge-triggered cell FDRE)</pre>
Destination:	<pre>rate_match_out[16] (output port)</pre>
Path Group:	(none)
Path Type:	Max at Slow Process Corner
Data Path Delay: Logic Levels:	4.979ns (logic 2.821ns (56.649%) route 2.159ns (43.351%)) 5 (FDRE=1 LUT5=1 LUT6=2 OBUF=1)

Figure.6 Max Delay Path



Power estimation from Synthesized derived from constraints files, simu vectorless analysis. Note: these ea change after implementation.	netiist. Activity lation files or rly estimates can	On-C	Chip Po	Sower Dynamic: 16.502 W (97%)					
Total On-Chip Power: Design Power Budget: Power Budget Margin: Junction Temperature:	16.999 W Not Specified N/A 48.8°C	9	97%	Signals: 0.883 W (5%) 94% Logic: 0.193 W (1%) UO: 15.426 W (94%) Device Static: 0.496 W (3%)					
Effective &JA:	36.2°C (24.6 W) 1.4°C/W								
Power supplied to off-chip devices:	0 W								
Confidence level:	Low								

Figure.7 Power Report

ADVANTAGES

Capacity-Achieving Performance

- Polar codes are provably capacity-achieving for symmetric binary-input memoryless channels, making them highly efficient for 5G NR control channels.
- They provide better error correction compared to traditional coding schemes like convolutional codes for short block lengths.

Enhanced Error Detection with CRC-Aided Decoding

- The addition of a Cyclic Redundancy Check (CRC) before encoding improves decoding performance by aiding list decoding (CA-SCL).
- It helps select the most reliable decoded path, reducing the probability of incorrect decoding.

Efficient Rate Matching for Dynamic Adaptation

- The rate matching process (puncturing, shortening, and repetition) allows encoded bits to adapt to varying channel conditions and transmission requirements.
- This ensures that polar-coded data can efficiently fit into available channel resources.

Standardized for 5G NR

- Polar codes have been adopted by 3GPP for 5G control channels, ensuring compatibility with global communication standards.
- Their use in uplink and downlink control information (UCI and DCI) ensures robust and reliable signaling.

Improved Performance Over Convolutional and LDPC Codes for Short Blocks

- Compared to convolutional codes (used in LTE control channels) and LDPC codes (used for 5G data channels), polar codes show better performance for short block lengths.
- This is crucial for low-latency applications like ultra-reliable low-latency communication (URLLC).

APPLICATIONS

1. 5G New Radio (5G NR) Communication



INTERNATIONAL JOURNAL OF APPLIED SCIENCE ENGINEERING AND MANAGEMEN

- Polar codes are used for Downlink and Uplink Control Information (DCI/UCI) in 5G NR, ensuring robust and efficient control signaling.
- The proposed system enhances error detection and correction, making it suitable for mission-critical applications such as industrial automation, autonomous vehicles, and smart grids.

2. Internet of Things (IoT) and Machine-Type Communication (MTC)

- IoT devices require efficient and low-power encoding schemes. Integrating CRC with Polar encoding reduces computational complexity, making it ideal for IoT networks.
- Supports large-scale IoT networks by ensuring reliable data transmission with optimized encoding and decoding processes.

3. Satellite and Space Communication

- The error resilience of the proposed system ensures reliable transmission in high-noise environments like space communication, where error correction is crucial.
- Integrated rate matching optimizes data transmission for limited bandwidth scenarios, improving spectral efficiency.

4. High-Speed Wireless Data Transmission

• The proposed system improves the performance of high-speed data transmission in applications like 4K/8K video streaming, augmented reality (AR), and virtual reality (VR) over 5G networks.

CONCLUSION

The implementation of Polar Encoding for 3GPP with Integrated CRC Generation and Rate Matching enhances the efficiency and reliability of 5G communication systems. By integrating CRC generation directly within the encoding process, the proposed system reduces computational complexity and improves error detection. Additionally, the optimized rate matching ensures better adaptability to varying channel conditions, leading to enhanced spectral efficiency and reduced latency.

This project plays a crucial role in modern wireless communication, IoT networks, satellite communication, and real-time applications by ensuring high-speed, reliable, and energy-efficient data transmission. The proposed approach not only simplifies hardware implementation but also enhances performance in 5G NR, URLLC, eMBB, and MTC applications.

Overall, this advancement in Polar encoding contributes to the ongoing evolution of next-generation wireless technologies, making it a critical component for future high-speed, secure, and low-latency communication networks.

FUTURE SCOPE

1. Enhancement in 6G and Beyond

- With the ongoing development of 6G wireless networks, the proposed system can be further optimized for higher data rates, lower latency, and enhanced reliability.
- Polar codes may evolve to handle even more massive connectivity, terahertz (THz) communications, and AI-driven adaptive encoding schemes.

2. AI-Optimized Polar Encoding

- Machine learning (ML) and deep learning (DL) can be integrated to enhance the efficiency of Polar encoding and decoding.
- AI-driven adaptive rate matching can dynamically adjust based on real-time network conditions, leading to better throughput and energy efficiency.

REFERENCES

- E. Arıkan, "Channel polarization: A method for constructing capacity-achieving codes for symmetric binary-input memoryless channels," IEEE Transactions on Information Theory, vol. 55, no. 7, pp. 3051– 3073, July 2009.
- 3GPP, "NR; Multiplexing and channel coding," 3rd Generation Partnership Project (3GPP), Technical Specification Group Radio Access Network, TS 38.212, v15.2.0, July 2018.
- 3. I. Tal and A. Vardy, "List decoding of polar codes," IEEE Transactions on Information Theory, vol. 61, no. 5, pp. 2213–2226, May 2015.
- K. Niu, K. Chen, and J. Lin, "Beyond turbo codes: Rate-compatible punctured polar codes for LTE and 5G," in Proc. IEEE International Conference on Communications (ICC), London, UK, June 2015, pp. 2991–2996.
- 5. A. Alamdar-Yazdi and F. R. Kschischang, "A simplified successive-cancellation decoder for polar codes," IEEE Communications Letters, vol. 15, no. 12, pp. 1378–1380, Dec. 2011.
- 6. B. Li, H. Shen, and D. Tse, "An adaptive successive cancellation list decoder for polar codes with cyclic redundancy check," IEEE Communications Letters, vol. 16, no. 12, pp. 2044–2047, Dec. 2012.
- 7. S. B. Korada, E. Şaşoğlu, and R. Urbanke, "Polar codes: Characterization of exponent, bounds, and constructions," IEEE Transactions on Information Theory, vol. 56, no. 12, pp. 6253–6264, Dec. 2010.
- P. Trifonov, "Efficient design and decoding of polar codes," IEEE Transactions on Communications, vol. 60, no. 11, pp. 3221–3227, Nov. 2012.
- C. Zhang and K. K. Parhi, "Low-latency sequential and overlapped architectures for successive cancellation polar decoder," IEEE Transactions on Signal Processing, vol. 61, no. 10, pp. 2429–2441, May 2013.
- A. Balatsoukas-Stimming, M. Bastani Parizi, and A. Burg, "LLR-based successive cancellation list decoding of polar codes," IEEE Transactions on Signal Processing, vol. 63, no. 19, pp. 5165–5179, Oct. 2015.